



CUSTOMER ADVISORY

ADV2319

Intel® Arria® V & Intel® Cyclone® V Datasheet Updates

Description:

Intel® is notifying customers of important updates to Intel® Arria® V and Intel® Cyclone® V Devices. Refer to Table 1 for the list of major updates and further details.

Table 1

#	Link to Updated Documents	Update Details
1)	Arria V Device Datasheet	<ul style="list-style-type: none">Updated the absolute VMAX for a receiver pin specification in the Receiver Specifications for Arria® V GX and SX Devices table. Refer to Table 22 of Page 26.Updated the absolute VMAX for a receiver pin specification in the Receiver Specifications for Arria® V GT and ST Devices table. Refer to Table 28 of Page 31.
2)	Cyclone V Device Datasheet	<ul style="list-style-type: none">Updated the absolute VMAX for a receiver pin specification in the Receiver Specifications for Cyclone® V GX, GT, SX, and ST Devices table. Refer to Table 23 of Page 27.

Recommended Actions:

Customers are requested to review the changes and determine the impact on their designs. Refer to the revision history of the updated documents for a complete list and history of updates.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected:

Intel Arria V and Cyclone V Devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://cdrdv2.intel.com/v1/dl/getContent/785817>

Contact:

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Customer Notifications Subscription

If you would like to receive customer notifications by email, please follow the instructions in [ADV 2209](#)

Intel references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.

Revision History

Date	Rev	Description
08/9/2023	1.0.0	Initial Release

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