



Customer Information Notification

202302012I : Release of Errata Document Revision "13" for LS1021A/20A/22A Family of Devices

Note: This notice is NXP Company Proprietary.

Issue Date: Jan 10, 2024 **Effective date:** Jan 11, 2024

Dear DigiKey Supplier Info PCNs,

Here is your personalized notification about a NXP general announcement.

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Management summary

Release of Errata document Revision "13" for LS1020A/21A/22A Family of Devices.

Change Category

Wafer Fab Process	Assembly Process	Product Marking	Test Process Design	
Wafer Fab Materials	Assembly Materials	Mechanical Specification	Test Equipment	Errata
Wafer Fab Location	Assembly Location	Packing/Shipping/Labeling	Test Location	Electrical spec./Test coverage
Firmware	Other			

PCN Overview

Description

The following changes have been made to the LS1020A, LS1021A and LS1022A Errata document:

The following errata were added:

- A-050757 - qDMA may hang on PCI Express outbound memory reads
- A-051238 - Hot plug-in/out on SATA Port Multiplier Ports (PMP) may not be detected
- A-050565 - A glitch on I/O pins during VDD core supply ramp up and ramp down
- A-050263 - SPI: Incorrect data may be transmitted in slave mode

The following errata were revised:

- A-011026 - Safe state is not removed from the channel output after fault condition ends if SWOCTRL register is used to control the pin
- A-009149 - USB 2.0 protocol data pins USB_D_M and USB_D_P do not support IEEE 1149.1
- A-007728 - The process of clearing the FTMx_SC[TOF] bit does not work as expected under a certain condition when the FTM counter reaches FTM_MOD value
- A-011218 - eDMA may not work with SPI
- A-008892 - QUICC Engine's DMA cannot address MURAM as target for reads/writes
- A-009798 - USB high speed squelch threshold adjustment

Reason

Notify customers of the errata updates.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist:

Position	Apps Manager
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NXP Quality Management Team.

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