| PCN Number: | | 20240122002.0 | | PCN Date: | January 23, 2024 |
|--------------------------|--|--------------------------|-------|------------------|---------------------|
| Title: | Title: Datasheet for MSPM0L130x and MSPM0L110x | | | | |
| Customer Contact: | | Change Management team | Dept: | Quality Services | |
| Change Type: | | Electrical Specification | | | |
| DOND 1 1 | | | | | |

PCN Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



MSPM0L1346, MSPM0L1345, MSPM0L1344, MSPM0L1343 MSPM0L1306, MSPM0L1305, MSPM0L1304, MSPM0L1303 SLASEXOD – OCTOBER 2022 – REVISED JANUARY 2024

| REVISION | NOTES |
|----------|--|
| D | Released WQFN16 package |
| | Improved wording of the descriptions |
| | Corrected the pinout issue for VSSOP28 TIA and WQFN16 |
| | Added the size of the pins for the leaded frame packages |
| | Added footnote for BOR in AMR table |
| | Added the specification for COMP settling time (static mode) |
| | Corrected the capacitor in Application schematics |
| | Changed the parameters for OPA PSRR and PGA |
| | Changed ADC minimum sampling time (without OPA) |
| | Corrected the temperature range for S version |
| | Changed the SPI low voltage |
| | Added the timer cross-trigger information |
| | Clarified for COMP and VBOOST testing condition |
| | Added a footnote for JEDEC J-STD-020 in AMR table |
| | Removed ADC-assisted CHOP in GPAMP |
| | |



MSPM0L1105, MSPM0L1106 SLASEX5C – OCTOBER 2022 – REVISED JANUARY 2024

| DATE | REVISION | NOTES | |
|--------------|----------|--|--|
| January 2024 | С | Released WQFN16 package | |
| | | Improved some wordings in the descriptions | |
| | | Fixed the pinout issue for WQFN16 | |
| | | Added the size of the pins for the leaded frame packages | |
| | | Added footnote for BOR in AMR table | |
| | | Fixed the capacitor in Application schematics | |
| | | Updated ADC minimum sampling time | |
| | | Updated the SPI low voltage | |
| | | Added the timer cross-trigger information | |
| | | Clarified for VBOOST testing condition | |
| | | Added a footnote for JEDEC J-STD-020 in AMR table | |
| | | Removed ADC-assisted CHOP in GPAMP | |

The datasheet number will be changing.

| Device Family | Change From: | Change To: |
|---------------|--------------|------------|
| MSPM0L130x | SLASEX0C | SLASEX0D |
| MSPM0L110x | SLASEX5B | SLASEX5C |

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/MSPM0L1306 http://www.ti.com/product/MSPM0L1105

Reason_for Change:

Product Affected:

MSPM0L1106TDGS28R

MSPM0L1106TDYYR

MSPM0L1106TRGER

MSPM0L1106TRHBR

MSPM0L1303SRGER

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device

Changes to product identification resulting from this PCN:

MSPM0L1304TDGS28R

MSPM0L1304TDYYR

MSPM0L1304TRGER

MSPM0L1304TRHBR

MSPM0L1305SDGS20R

None.

| MSPM0L1105TDGS20R | MSPM0L1304SDGS20R | MSPM0L1305SDYYR | MSPM0L1306SRGER |
|--------------------|--------------------|-------------------|-------------------|
| MSPM0L1105TDGS28R | MSPM0L1304SDGS28R | MSPM0L1305SRGER | MSPM0L1306SRHBR |
| MSPM0L1105TDYYR | MSPM0L1304SDYYR | MSPM0L1305SRHBR | MSPM0L1306TDGS20R |
| MSPM0L1105TRGER | MSPM0L1304SRGER | MSPM0L1305TDGS20R | MSPM0L1306TDGS28R |
| MSPM0L1105TRHBR | MSPM0L1304SRHBR | MSPM0L1305TDGS28R | MSPM0L1306TDYYR |
| MSPM0I 1106TDGS20R | MSPM0I 1304TDGS20R | MSPMOL1305TDYYR | MSPMOL1306TRGFR |

MSPM0L1305TRGER

MSPM0L1305TRHBR

MSPM0L1306SDGS20R

MSPM0L1306SDGS28R

MSPM0L1306SDYYR

MSPM0L1303TRGER MSPM0L1305SDGS28R

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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MSPM0L1306TRHBR

MSPM0L1343TDGS20R

MSPM0L1344TDGS20R

MSPM0L1345TDGS28R

MSPM0L1346TDGS28R

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